

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)

Application Number	10805596
Filing Date	2004-03-19
First Named Inventor	Chieng et al.
Art Unit	2193
Examiner Name	Yaary, Michael D.
Attorney Docket Number	ELAN-01187US1

U.S.PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code†	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	5481568		1996-01-02	Yada	
	2	5986589		1999-11-16	Rosefield et al.	
	3	6061410		2000-05-09	Linz	
	4	6134268		2000-10-17	McCoy	
	5	6531970		2003-03-11	McLaughlin et al.	
	6	6593807		2003-07-15	Groves et al.	
	7	7023868		2006-04-04	Rabenko et al.	
	8	7167112		2007-01-23	Andersen et al.	

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9	74774722		2009-01-06	Chieng et al.	
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U.S.PATENT APPLICATION PUBLICATIONS

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	1	20020105448		2002-08-08	Friedhof	
	2	20030179116		2003-09-25	Oki	

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FOREIGN PATENT DOCUMENTS

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	1	GB 2 267 193	GB		1993-11-24	Sony UK Ltd.		<input type="checkbox"/>
	2	JP 2002-314429	JP		2002-10-25	Sony Corp.		<input checked="" type="checkbox"/>
	3	JP 2008-018518	JP		2008-01-31	Fujitsu Hitachi Plasma Display		<input checked="" type="checkbox"/>

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1	KETOLA et al., "Synchronization of Fractional Interval Counter in Non-Integer Ratio Sample Rate Converters," Proceedings of the 2003 International Symposium on Circuits and Systems, 2003 ISCAS '03, vol. 2, May 25-28, 2003, pp. 89-92.	<input type="checkbox"/>
2	PARK et al., "A Novel Structure for Real-Time Digital Sample-Rate Converters with Finite Precision Error Analysis," Speech Processing 2, VLSI, Underwater Signal Processing, Toronto, May 14-17, 1991, International Conference on Acoustics, Speech and Signal Processing, ICASSP, NY, IEEE, US, Vol. 2, Conf. 16, April 14, 1991, 4 pages.	<input type="checkbox"/>

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